

In the claims:

Please cancel claims 1, 7 and 15 without prejudice.

For the Examiner's convenience, all pending claims are presented below with changes shown in accordance with the new mandatory amendment format.

- 1 1. (Cancelled)
- 1 2. (Currently Amended) The system of claim 1 8 wherein the current limiter  
2 prevents excess current from flowing from the SC to the battery.
- 1 3. (Currently Amended) The system of claim 1 8 wherein the SC prevents transients  
2 from the computer system from affecting the battery voltage.
- 1 4. (Previously Presented) The system of claim 3 wherein the SC has a  
2 capacitance of 20 farad and a resistance of 5 m .
- 1 5. (Currently Amended) The system of claim 1 8 wherein the computer system  
2 comprises:  
3 a power delivery subsection; and  
4 a plurality of hardware components coupled to the power delivery subsection.
- 1 6. (Original) The system of claim 5 wherein the power delivery subsection  
2 comprises:  
3 a system voltage regulator;  
4 a chipset voltage regulator; and  
5 a central processing unit (CPU) voltage regulator.

7. (Cancelled)

8. (Currently Amended) ~~The system of claim 7 wherein the current limiter further comprises:~~

~~a first comparator with inputs coupled across the resistor and an output coupled to the gate of the second transistor; and~~

~~a second comparator with inputs coupled across the resistor and an output coupled to the gate of the first transistor.~~

A system comprising:

a battery;

a super-capacitor (SC) coupled in parallel to the battery;

a computer system coupled to the battery and the SC; and

a current limiter, coupled to the battery, comprising:

a first transistor coupled to the battery;

a second transistor coupled to the first transistor to prevent excess current from flowing from the battery to the SC whenever the second transistor is deactivated; and

a resistor coupled to the second transistor, the SC and the computer system.

a first comparator with inputs coupled across the resistor and an output coupled to the gate of the second transistor; and

a second comparator with inputs coupled across the resistor and an output coupled to the gate of the first transistor.

1 9. (Original) The system of claim 8 wherein the first comparator deactivates the  
2 second transistor if the voltage across the resistor is greater than a first predetermined  
3 threshold.

1 10. (Original) The system of claim 9 wherein the second comparator deactivates  
2 the first transistor if the voltage across the resistor is greater than a second predetermined  
3 threshold.

1 11. (Currently Amended) A system comprising:  
2 a battery;  
3 a super-capacitor (SC) coupled in parallel to the battery;  
4 a power delivery system coupled to the battery and the SC; and  
5 ~~a current limiter, coupled to the battery, the SC and the power delivery system, to~~  
6 ~~prevent excess current from flowing from the battery to the SC.~~

7 a current limiter, coupled to the battery, comprising:  
8 a first transistor coupled to the battery;  
9 a second transistor coupled to the first transistor to prevent excess current  
10 from flowing from the battery to the SC whenever the second transistor is  
11 deactivated; and

12 a resistor coupled to the second transistor, the SC and the computer  
13 system.

14 a first comparator with inputs coupled across the resistor and an output  
15 coupled to the gate of the second transistor; and

16                    a second comparator with inputs coupled across the resistor and an output  
17                    coupled to the gate of the first transistor.

1    12.    (Original)    The system of claim 11 wherein the current limiter prevents excess  
2    current from flowing from the SC to the battery.

1    13.    (Previously Presented)    The system of claim 11 wherein the SC prevents  
2    transients from the computer system from affecting the battery voltage.

1    14.    (Original)    The system of claim 11 wherein the power delivery system  
2    comprises:

3                    a first voltage regulator; and

4                    a second voltage regulator.

1    15.    (Cancelled)

1    16.    (Currently Amended) ~~The current limiter of claim 15 further comprising:~~

2                    ~~a first comparator with inputs coupled across the resistor and an output coupled to~~  
3    ~~the gate of the second transistor; and~~

4                    ~~a second comparator with inputs coupled across the resistor and an output coupled~~  
5    ~~to the gate of the first transistor. A current limiter comprising:~~

6                    a first transistor coupled to a battery;

7                    a second transistor coupled to the first transistor to prevent excess current from  
8    flowing from the battery whenever the second transistor is deactivated;

9                    a resistor coupled to the second transistor;

10        a first comparator with inputs coupled across the resistor and an output coupled to  
11        the gate of the second transistor; and  
12        a second comparator with inputs coupled across the resistor and an output coupled  
13        to the gate of the first transistor.

1        17.     (Original)     The current limiter of claim 16 wherein the first comparator  
2        deactivates the second transistor if the voltage across the resistor is greater than a first  
3        predetermined threshold.

1        18.     (Original)     The current limiter of claim 17 wherein the second comparator  
2        deactivates the first transistor if the voltage across the resistor is greater than a second  
3        predetermined threshold.